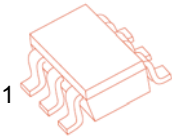
dual digital transistors (PNP+PNP)

SOT-363

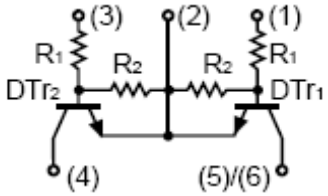


FEATURES

- Two DTA114Y chips in a package

Marking: G5

Equivalent circuit



Absolute maximum ratings (T_a=25℃)

Symbol	Parameter	Value	Units
V_{CC}	Supply Voltage	50	V
$I_{C(MAX)}$	Output Current	100	mA
V_i	Input Voltage	-6 to +40	V
P_D	Power Dissipation	150	mW
T_J	Junction Temperature	150	℃
T_{stg}	Storage Temperature	-55~+150	℃

Electrical Characteristics (T_a=25℃)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Input turn-on voltage	$V_{i(on)}$	V_O=0.3V, I_O=1mA			1.4	V
Input cut-off voltage	$V_{i(off)}$	V_{CC}=5V, I_O=100μA	0.3			V
Output voltage	$V_{O(on)}$	I_O=5mA, I_i=0.25 mA			0.3	V
Input cut-off current	I_i	V_i =5V			0.88	mA
Output cut-off current	$I_{O(off)}$	V_{CC}=50V, V_i=0			0.5	μA
DC current gain	G_i	V_O =5V, I_O=5mA	68			
Transition frequency	f_T	V_O =10V, I_O=5mA, f =100MHz		250		MHz
Input resistance	R_1		7		13	KΩ
Resistance ratio	R_2/R_1		3.7		5.7	